

(19) 世界知的所有権機関
国際事務局



(43) 国際公開日
2003 年 5 月 1 日 (01.05.2003)

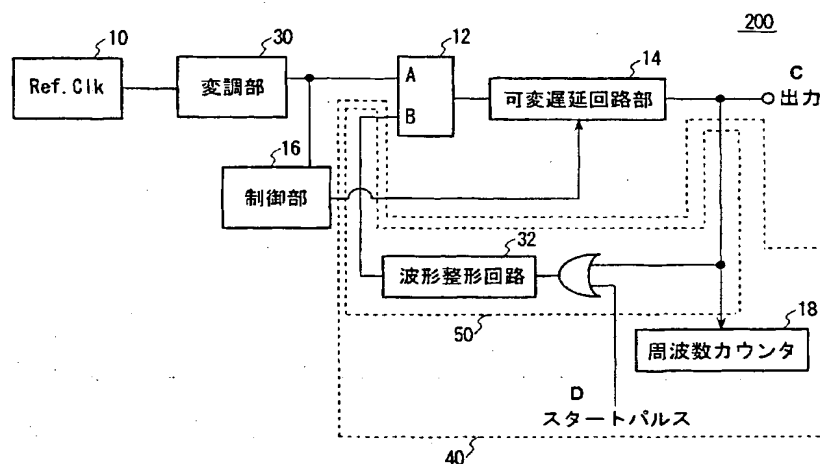
PCT

(10) 国際公開番号
WO 03/036314 A1

- (51) 国際特許分類: G01R 31/28, H03L 7/06, H03K 5/00 (72) 発明者: および
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(21) 国際出願番号: PCT/JP02/11053 (OKAYASU, Toshiyuki) [JP/JP]; 〒179-0071 東京都 練馬区 旭町 1 丁目 3 番 1 号 株式会社アドバンテスト内 Tokyo (JP).
(22) 国際出願日: 2002 年 10 月 24 日 (24.10.2002)
(25) 国際出願の言語: 日本語 (74) 代理人: 龍華 明裕 (RYUKA, Akihiro); 〒160-0022 東京都 新宿区 新宿 1 丁目 2 4 番 1 2 号 東信ビル 6 階 Tokyo (JP).
(26) 国際公開の言語: 日本語
(30) 優先権データ: 特願 2001-326500 (81) 指定国 (国内): DE, KR, US.
2001 年 10 月 24 日 (24.10.2001) JP 添付公開書類:
— 国際調査報告書
(71) 出願人 (米国を除く全ての指定国について): 株式会社アドバンテスト (ADVANTEST CORPORATION) [JP/JP]; 〒179-0071 東京都 練馬区 旭町 1 丁目 3 番 1 号 Tokyo (JP).
2 文字コード及び他の略語については、定期発行される各 PCT ガゼットの巻頭に掲載されている「コードと略語のガイダンスノート」を参照。

(54) Title: TIMING GENERATOR, SEMICONDUCTOR TESTING DEVICE, AND TIMING GENERATING METHOD

(54) 発明の名称: タイミング発生器、半導体試験装置、及びタイミング発生方法



30... MODULATION UNIT
14... VARIABLE DELAY CIRCUIT UNIT
C... OUTPUT
16... CONTROL UNIT
32... WAVEFORM SHAPING CIRCUIT
18... FREQUENCY COUNTER
D... START PULSE

(57) Abstract: A timing generator comprising a reference signal generating unit for generating a specified frequency, a variable delay circuit unit for outputting a timing signal delayed a specified time from a reference signal, and a delay measuring unit for measuring a delay by the variable delay circuit unit, a delay by the variable delay circuit unit being controlled based on a delay measured by the delay measuring unit, wherein the frequency of a reference signal is continuously

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP02/11053

A. CLASSIFICATION OF SUBJECT MATTER

Int.Cl.⁷ G01R31/28, H03L7/06, H03K5/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int.Cl.⁷ G01R31/28, H03L7/06, H03K5/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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Kokai Jitsuyo Shinan Koho	1971-2003	Toroku Jitsuyo Shinan Koho	1994-2003

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 9-119962 A (Advantest Corp.), 06 May, 1997 (06.05.97), Full text; all drawings & US 5764598 A & DE 19644283 A1	1-5, 8-11
A	US 5942902 A (Advantest Corp.), 24 August, 1999 (24.08.99), Full text; all drawings & JP 9-171061 A Full text; all drawings & DE 19652890 A1 & DE 19655110 A1 & KR 97048486 A & CN 1159586 A	1-11
A	JP 9-304488 A (Advantest Corp.), 28 November, 1997 (28.11.97), Full text; all drawings (Family: none)	1-11

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	"I" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E" earlier document but published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search
28 January, 2003 (28.01.03)

Date of mailing of the international search report
12 February, 2003 (12.02.03)

Name and mailing address of the ISA/
Japanese Patent Office

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